

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT <i>All references considered</i>			ATTY. DOCKET NO. 1778.0100002 (0055.20US)		APPLICATION NO. 09/662,832	
			APPLICANT Van Hook et al.			
			FILING DATE September 15, 2000		GROUP 2183	

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>W</i>	AA3	5,581,773	12/1996	Glover	395	800	
<i>W</i>	AB3	5,590,345	12/1996	Barker et al.	395	800	
<i>W</i>	AC3	5,666,298	09/1997	Peleg et al.	364	715.08	
<i>W</i>	AD3	5,669,010	09/1997	Duluk, Jr.	395	800.22	
<i>W</i>	AE3	5,721,892	02/1998	Peleg et al.	395	562	
<i>W</i>	AF3	5,734,874	03/1998	Van Hook et al.	395	513	
<i>W</i>	AG3	5,740,340	04/1998	Purcell et al.	395	118	
<i>W</i>	AH3	5,752,071	05/1998	Tubbs et al.	395	800.34	
<i>W</i>	AI3	5,758,176	05/1998	Agarwal et al.	395	800	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ3						Yes No
	AK3						Yes No
	AL3						Yes No
	AM3						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>W</i>	AN	3	Peleg, A. and Weiser, U., "MMX Technology Extension to the Intel Architecture," <i>IEEE Micro</i> , IEEE, pp. 42-50 (August 1996).
<i>W</i>	AO	3	QML-DSP/MCM and Die Courier, at <a href="http://www.ti.com/sc/docs/military/liter/ecour/dsp.htm">http://www.ti.com/sc/docs/military/liter/ecour/dsp.htm</a> , Texas Instruments, Inc., 7 pages (January 1996).
<i>W</i>	AP	3	Turley, J. and Hakkarainen, H., "TI's New 'C6x DSP Screams at 1,600 MIPS," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 14-17 (February 17, 1997).
<i>W</i>	AQ	3	DSP56002 24-Bit Digital Signal Processor Semiconductor Technical Data, Motorola, Inc., 110 pages (1996). (Bates Numbers L07803-L07912).
<i>W</i>	AR	3	A236 Parallel Digital Signal Processor Chip Programmer's Reference Manual, Oxford Micro Devices, Inc., 195 pages (1994). (Bates Numbers L11184-L11378).

EXAMINER

DATE CONSIDERED

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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>OTPE JCI</i>	AA4	5,761,523	06/1998	Wilkinson et al.	395	800.2	
<i>MAY 1 2002</i>	AB4	5,778,241	07/1998	Bindloss et al.	395	800.2	
<i>PENT &amp; TRADEMARK SEARCH</i>	AC4	5,784,602	07/1998	Glass et al.	395	561	
<i>N</i>	AD4	5,793,661	08/1998	Dulong et al.	364	750.5	
<i>N</i>	AE4	5,812,147	09/1998	Van Hook et al.	345	511	
<i>N</i>	AF4	5,812,723	09/1998	Ohtsu et al.	385	128	
<i>N</i>	AG4	5,815,723	09/1998	Wilkinson et al.	395	800.2	
<i>N</i>	AH4	5,822,606	10/1998	Morton	395	800.16	
<i>N</i>	AI4	5,838,984	11/1998	Nguyen et al.	395	800.05	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ4						Yes No
	AK4						Yes No
	AL4						Yes No
	AM4						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>N</i>	AN	4	Lee, R., "Accelerating Multimedia with Enhanced Microprocessors," <i>IEEE Micro</i> , IEEE, pp. 22-32 (April 1995). (Bates Numbers L08566-L08576).
<i>N</i>	AO	4	<i>DSP56000 24-Bit Digital Signal Processor Family Manual</i> , Motorola, Inc., 638 pages (1995). (Bates Numbers L08722-L09359).
<i>N</i>	AP	4	<i>Microprocessor Devices Reliability Monitor Report: Third Quarter 1997</i> , Motorola, 8 pages (1997).
<i>N</i>	AQ	4	<i>Reliability Monitor Environmental Stress Data 3Q97</i> , Motorola, 4 pages (1997).
<i>N</i>	AR	4	<i>Reliability and Quality Report 3Q97</i> , Motorola, 2 pages (1997).

EXAMINER	<i>M.J.P.</i>	DATE CONSIDERED
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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
<i>OIPEN</i>	AA5	5,864,703	01/1999	van Hook et al.	395	800.22	
<i>n</i>	AB5	5,881,307	03/1999	Park et al.	395	800.23	
<i>MAY 12 2002</i>	AC5	5,936,872	08/1999	Fischer et al.	364	754.03	
<i>n</i>	AD5	5,960,012	09/1999	Spracklen	371	53	
<i>n</i>	AE5	5,996,056	11/1999	Volkonsky	712	1	
<i>n</i>	AF5	6,006,316	12/1999	Dinkjian	712	22	
<i>n</i>	AG5	6,058,465	05/2000	Nguyen	712	7	
<i>n</i>	AH5	6,088,783	07/2000	Morton	712	22	
	AI5						

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ5						Yes No
	AK5						Yes No
	AL5						Yes No
	AM5						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>n</i>	AN	5	<i>Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997</i> , Motorola, 10 pages (1997).
<i>n</i>	AO	5	<i>Reliability and Quality Report 4Q97</i> , Motorola, 2 pages (1997).
<i>n</i>	AP	5	"Hardware Implications of xmem as a st followed by a ld," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, 5 pages (September 1992).
<i>n</i>	AQ	5	<i>601 First Silicon</i> , at <a href="http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html">http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html</a> , Motorola, 2 Pages (October 1, 1992).
<i>n</i>	AR	5	<i>DSP56002/DSP56L002 24-Bit Digital Signal Processor Motorola Semiconductor Product Information</i> , Motorola, Inc., 3 pages (1994). (Bates Numbers L07913-L07915).

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			APPLICANT Van Hook et al.	FILING DATE September 15, 2000
			GROUP 2183	

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
AA6						
AB6						
AC6						
AD6						
AE6						
AF6						
AG6						
AH6						
AI6						

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
AJ6						Yes No
AK6						Yes No
AL6						Yes No
AM6						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>h</i>	AN	6	MC88410UM/AD: MC88410 Secondary Cache Controller User's Manual, at <a href="http://www.mot-sps.com/lit/html/mc88410umad.html">http://www.mot-sps.com/lit/html/mc88410umad.html</a> , Motorola, Inc., 1 page (January 24, 1997).
<i>h</i>	AO	6	AN1214: MC88110 64-bit External Bus Interface to 16-bit EPROM, at <a href="http://www.mot-sps.com/lit/html/an1214.html">http://www.mot-sps.com/lit/html/an1214.html</a> , Motorola, Inc., 1 page (January 24, 1997).
<i>h</i>	AP	6	TMS320C80 Digital Signal Processor, Texas Instruments Corp., p. 41 (July 1994).
<i>h</i>	AQ	6	EB162: Programming Tips (MC88110), at <a href="http://www.mot-sps.com/lit/html/eb162.html">http://www.mot-sps.com/lit/html/eb162.html</a> , Motorola, Inc., 1 page (January 24, 1997).
<i>h</i>	AR	6	MC88110/410DH/AD: MC88110/MC88410 Designer's Handbook, at <a href="http://www.mot-sps.com/lit/html/mc88110410dhad.html">http://www.mot-sps.com/lit/html/mc88110410dhad.html</a> , Motorola, Inc., 1 page (January 24, 1997).

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/662,832	09/15/2000	Timothy J. van Hook	1778.0100002	2552

7590 12/19/2002

Sterne Kessler Goldstein & Fox PLLC  
Attorneys at Law  
1100 New York Avenue N W Suite 600  
Washington, DC 20005-3934

EXAMINER

PAN, DANIEL H

ART UNIT

PAPER NUMBER

2183

DATE MAILED: 12/19/2002

20

Please find below and/or attached an Office communication concerning this application or proceeding.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

# FEE TRANSMITTAL for FY 2002

Patent fees are subject to annual revision.

**TOTAL AMOUNT OF PAYMENT****\$740.00****Complete If Known**

<b>Application Number</b>	09/662,832
<b>Filing Date</b>	September 15, 2000
<b>First Named Inventor</b>	VAN HOOK et al.
<b>Examiner Name</b>	Pan, D.
<b>Group Art Unit</b>	2183
<b>Attorney Docket No.</b>	1778.0100002

**METHOD OF PAYMENT (check one)**

1.  The Commissioner is hereby authorized to charge indicated fees and credit any overpayment to:

Deposit Account Number **19-0036**  
Deposit Account Name **Sterne, Kessler, Goldstein & Fox P.L.L.C.**

Charge Any Additional Fee Required  
Under 37 CFR §§ 1.16 and 1.17

Applicant claims small entity status  
See 37 CFR 1.27

2.  Payment Enclosed:

Check  Credit card  Money Order  Other\*

\*Charge any deficiencies or credit any overpayments in the fees or fee calculations of Parts 1, 2 and 3 below to Deposit Account No. 19-0036.

**FEE CALCULATION (continued)**

**3. ADDITIONAL FEES**  
**Large Entity      Small Entity**

Fee Code	Fee (\$)	Fee Code	Fee (\$)	Fee Description	Fee paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for ex parte reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	
116	400	216	200	Extension for reply within second month	
117	920	217	460	Extension for reply within third month	
118	1,440	218	720	Extension for reply within fourth month	
128	1,960	228	980	Extension for reply within fifth month	
119	320	219	160	Notice of Appeal	
120	320	220	160	Filing a brief in support of an appeal	
121	280	221	140	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,280	241	640	Petition to revive - unintentional	
142	1,280	242	640	Utility issue fee (or reissue)	
143	460	243	230	Design issue fee	
144	620	244	310	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	130	123	130	Petitions related to provisional applications	
126	180	126	180	Submission of Information Disclosure Stmt	
581	40	481	40	Recording each patent assignment per property (times number of properties)	
146	740	246	370	Filing a submission after final rejection (37 CFR 1.129(a))	
149	740	249	370	For each additional invention to be examined (37 CFR 1.129(b))	
179	740	279	370	Request for Continued Examination (RCE)	\$740.00
169	900	169	900	Request for expedited examination of a design application	

Other fee (specify):

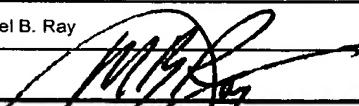
Other fee (specify):

\*Reduced by Basic Filing Fee Paid

**SUBTOTAL (3)      (\$ 740.00)**

\*\* or number previously paid, if greater; For Reissues, see above

**SUBMITTED BY****Complete (if applicable)**

Name (Print/Type)	Michael B. Ray	Registration No. (Attorney/Agent)	33,997	Telephone	202-371-2600
Signature				Date	5/22/02

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SKGF Rev. 10/01/01 mnac

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*All References considered.*

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL <i>OIRE SCJ</i>	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA7					
<i>MAY 22 2002</i> <i>PATENT &amp; TRADEMARK OFFICE</i>	AB7					
	AC7					
	AD7					
	AE7					
	AF7					
	AG7					
	AH7					
	AI7					

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ7					Yes No
	AK7					Yes No
	AL7					Yes No
	AM7					Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>CR</i>	AN	7	MC88110UMAD: MC88110 Second Generation RISC Microprocessor User's Manual, at <a href="http://www.mot-sps.com/lit/html/mc88110umad.html">http://www.mot-sps.com/lit/html/mc88110umad.html</a> , Motorola, Inc., 1 page (January 24, 1997).
<i>W</i>	AO	7	Weiss, R., ARM Piccolo Mixes ARM RISC with DSP, at <a href="http://www.estd.com/Editorial/1996/11/Briefs/arm.html">http://www.estd.com/Editorial/1996/11/Briefs/arm.html</a> , 1 page, (November 1996).
<i>h</i>	AP	7	TMS320C1x/C2x/C2xx/C5x Assembly Language Tools User's Guide, Texas Instruments, 483 pages (March 1995). (Bates Numbers L07916-L08398).
<i>n</i>	AQ	7	TMS320C5x General-Purpose Applications User's Guide, Texas Instruments, 167 pages (July 1997). (Bates Numbers L08399-L08565).
<i>n</i>	AR	7	EI-Sharkawy, Mohamed, Ph.D., Digital Signal Processing Applications with Motorola's DSP56002 Processor, Prentice Hall PTR, pp. 43-124 (1996). (Bates Numbers L06519-L06601).

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<i>OIPE</i>	AA8						
	AB8						
<i>MAY 22 2002</i> <i>PATENT &amp; TRADEMARK OFFICE</i>	AC8						
	AD8						
	AE8						
	AF8						
	AG8						
	AH8						
	AI8						

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	AJ8						Yes No
	AK8						Yes No
	AL8						Yes No
	AM8						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>n</i>	AN	<u>8</u>	Killian, E., "MIPS Extensions for Digital Media," <i>Microprocessor Forum</i> , pp. 1- 5 (October 22-23, 1996).
<i>n</i>	AO	<u>8</u>	"Interrupt Latency in the MC88110," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, pp. 1, 2 and 4-9 (1992).
<i>n</i>	AP	<u>8</u>	"Running the MC88110 in Lockstep," <i>Motorola Semiconductor Engineering Bulletin</i> , Motorola, 2 pages (1992).
<i>n</i>	AQ	<u>8</u>	Turley, J., "Siemens TriCore Revives CISC Techniques: New 32-Bit Design Emphasizes DSP Capability and Microcontroller Functions," <i>Microprocessor Report</i> , Microdesign Resources, pp. 13-16 (November 17, 1997).
<i>n</i>	AR	<u>8</u>	<i>i860™ Microprocessor Family Programmer's Reference Manual</i> , Intel Corporation, 79 pages (1992). (Bates Numbers L09361-L09439).

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